

## EAST Search History

| Ref # | Hits | Search Query   | DBs      | Default Operator | Plurals | Time Stamp       |
|-------|------|--|----------|------------------|---------|------------------|
| L9    | 1    | (PRINTED ADJ CIRCUIT ADJ BOARD AND CONDUCTIVE ADJ PATHS AND SWITCHING ADJ DEVICE).CLM. | US-PGPUB | OR               | OFF     | 2006/10/11 14:22 |

## EAST Search History

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| S80 2 | 18   | printed adj circuit adj board same layers and path adj resistance | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 08:42 |
| S80 5 | 11   | printed adj circuit and path adj resistance and ripple            | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 08:45 |
| S80 7 | 395  | printed adj circuit and vias and path adj length                  | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 08:46 |
| S80 8 | 27   | printed adj circuit and vias and path and resistance adj length   | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 08:48 |
| S80 9 | 58   | printed adj circuit and vias and path adj resistance              | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 08:51 |
| S81 1 | 632  | printed adj circuit and vias and path and balance                 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 08:52 |
| S81 7 | 21   | three adj phase and printed adj circuit and vias and switch       | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 08:59 |
| S82 0 | 17   | printed adj circuit and vias and path adj conductivity            | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 09:00 |
| S82 1 | 223  | printed adj circuit and vias and electrical adj length            | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | OFF     | 2006/10/11 09:11 |

## EAST Search History

|          |     |   |   |    |     |                  |
|----------|-----|---|---|----|-----|------------------|
| S82<br>3 | 37  | printed adj circuit same vias same delay            | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 09:23 |
| S82<br>4 | 3   | printed adj circuit same vias same path adj lengths | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 09:23 |
| S82<br>5 | 59  | printed adj circuit same vias and path adj lengths  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 09:30 |
| S82<br>7 | 854 | 361/777.CCLS.                                       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 09:32 |
| S82<br>8 | 193 | 361/777.CCLS. AND VIAS                              | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 09:40 |
| S82<br>9 | 395 | 257/786.CCLS. AND VIAS                              | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 09:45 |
| S83<br>0 | 491 | 174/261.CCLS. AND VIAS                              | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 09:45 |
| S83<br>1 | 5   | 174/261.CCLS. AND VIAS AND PATH ADJ LENGTH          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/10/11 13:51 |